

The diagram shows a network node 300 with the following components and connections:

- PHY RECEIVE** (top left) receives an external input and connects to the top **MEMORY** block (301).
- MEMORY** (301) connects to **INPUT QUEUES** (top right).
- FE** (FORWARDING ENGINE, top center) receives input from **PHY RECEIVE** and **INPUT QUEUES**.
- QOS** (SOFTWARE/FIRMWARE, bottom center) receives input from the **FE** and **OUTPUT QUEUES**.
- QOS** connects to the bottom **MEMORY** block (302).
- MEMORY** (302) connects to the **PHY TRANSMIT** block (bottom left).
- PHY TRANSMIT** sends an external output.
- OUTPUT QUEUES** (bottom right) receive input from **FROM SWITCH** and connect to the **QOS**.

FIG. 4A

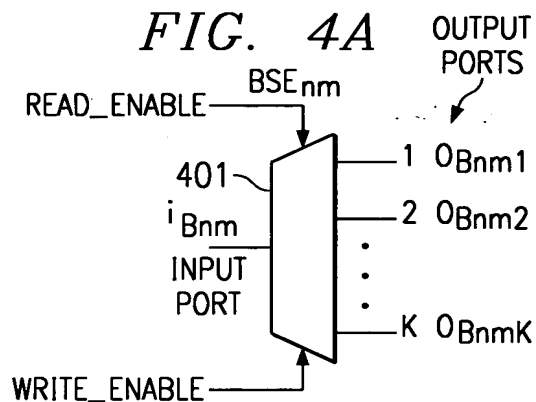


FIG. 4B

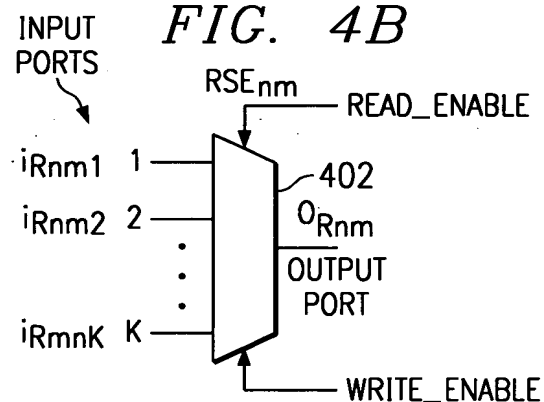


FIG. 5A

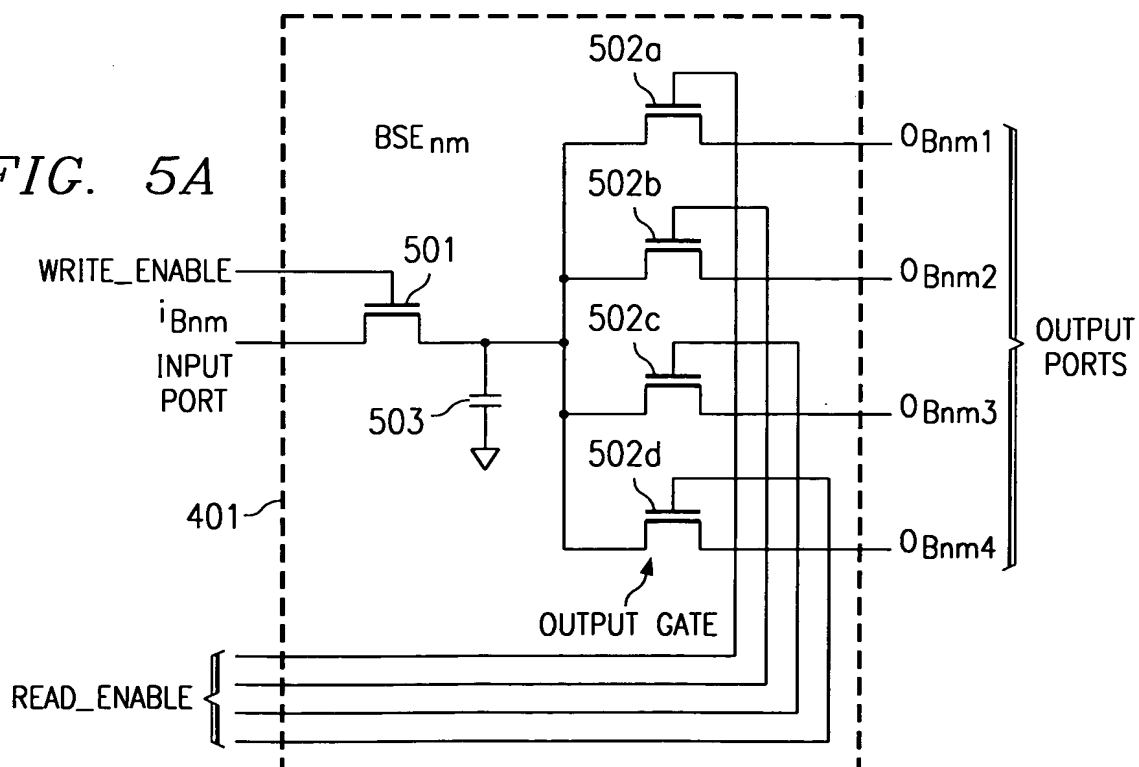


FIG. 5B

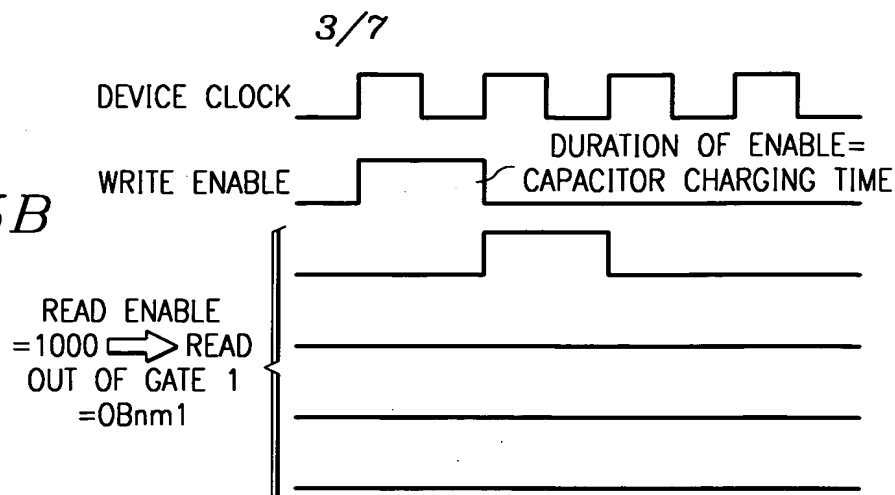


FIG. 6A

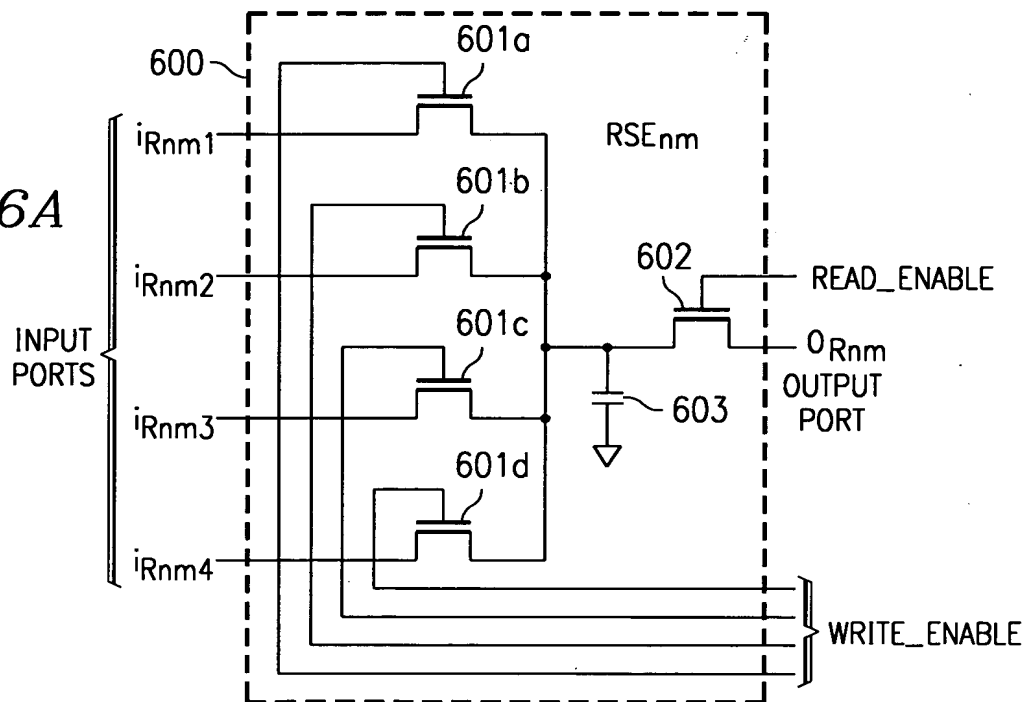
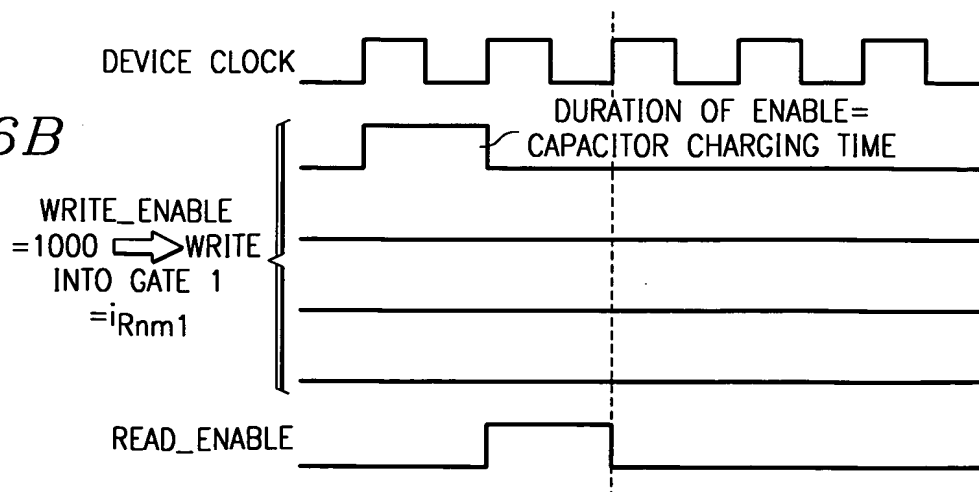
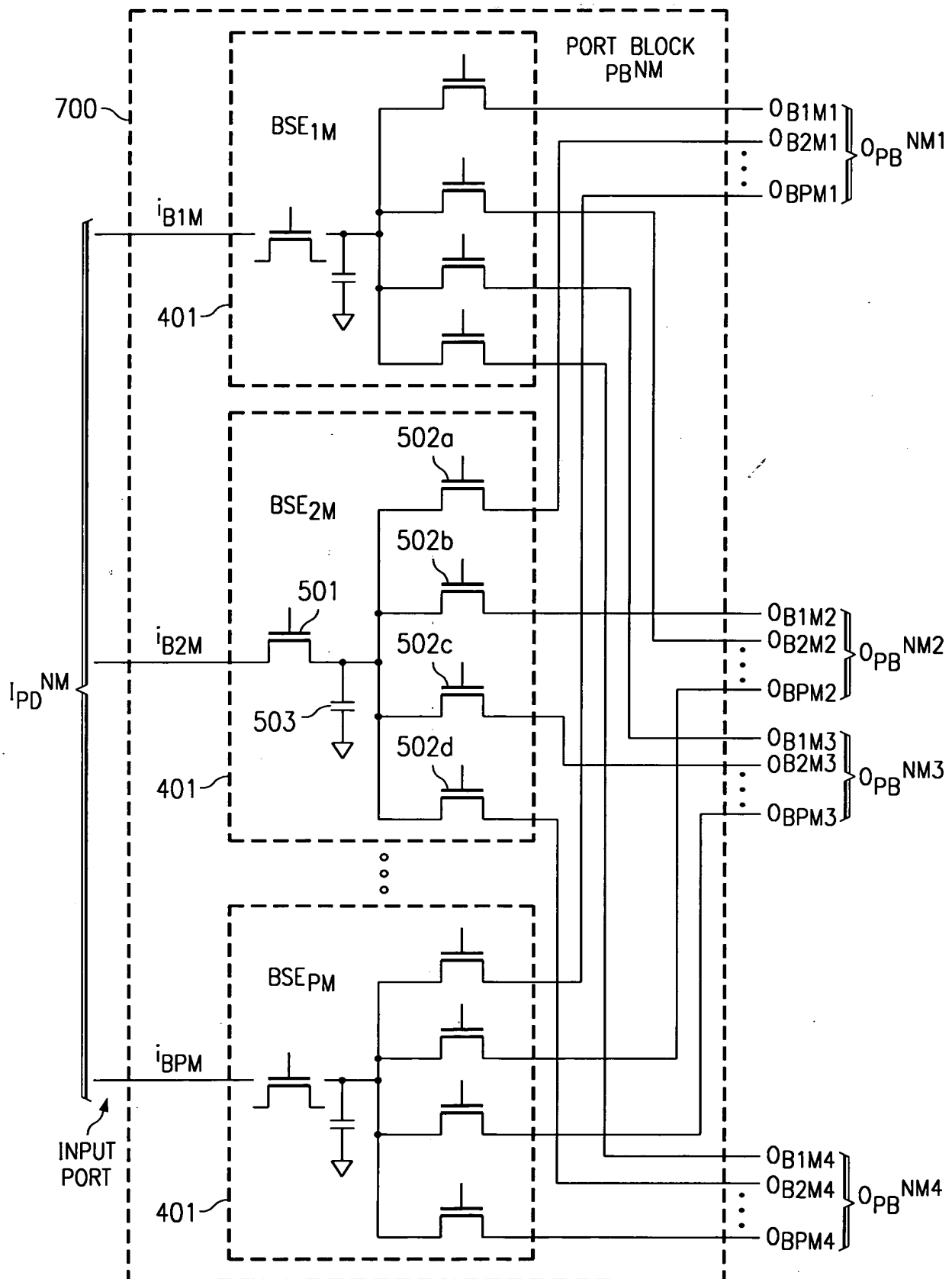


FIG. 6B



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FIG. 7



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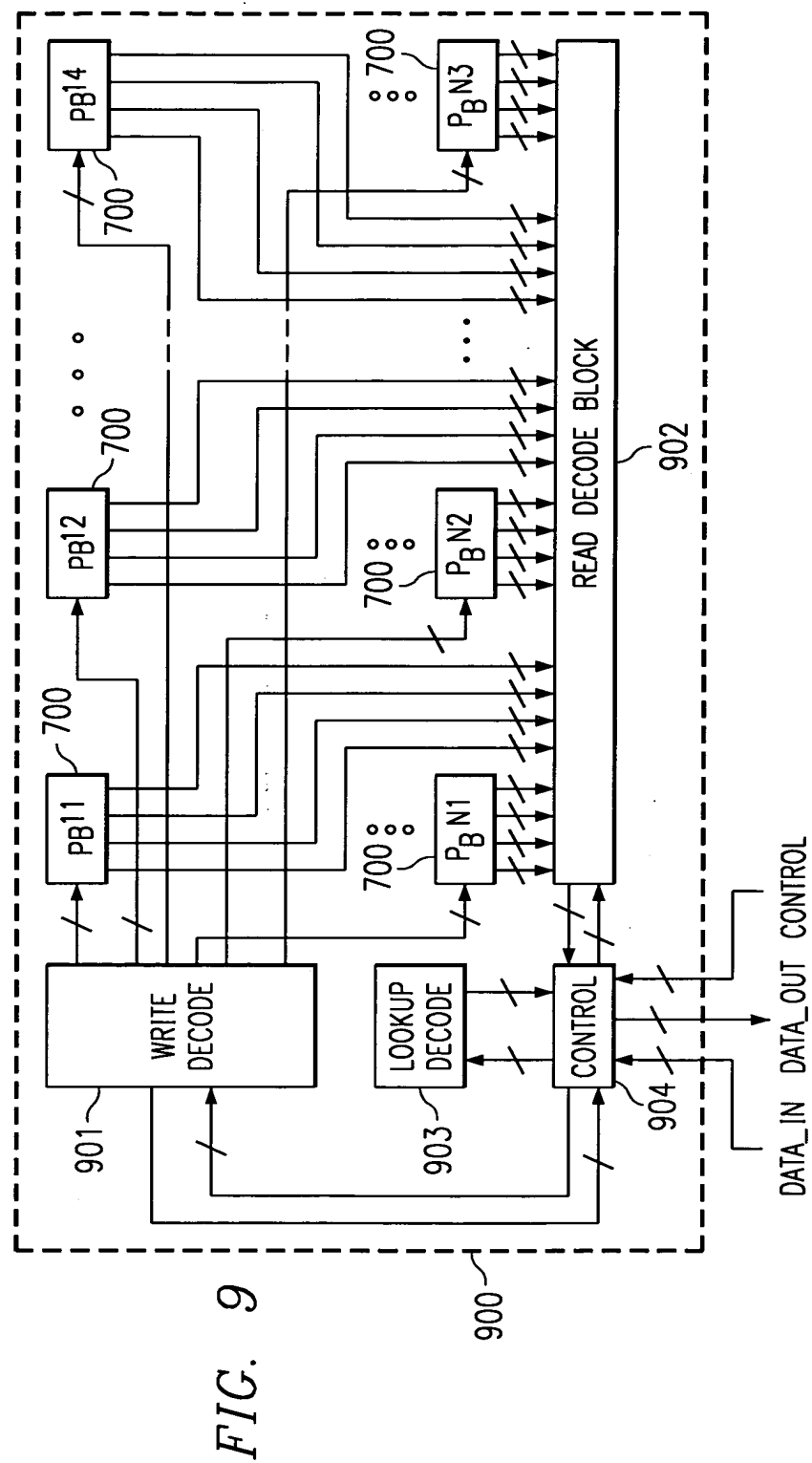
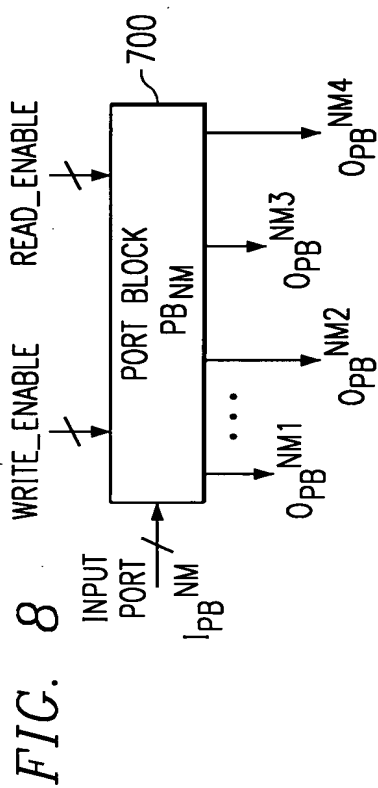


FIG. 10

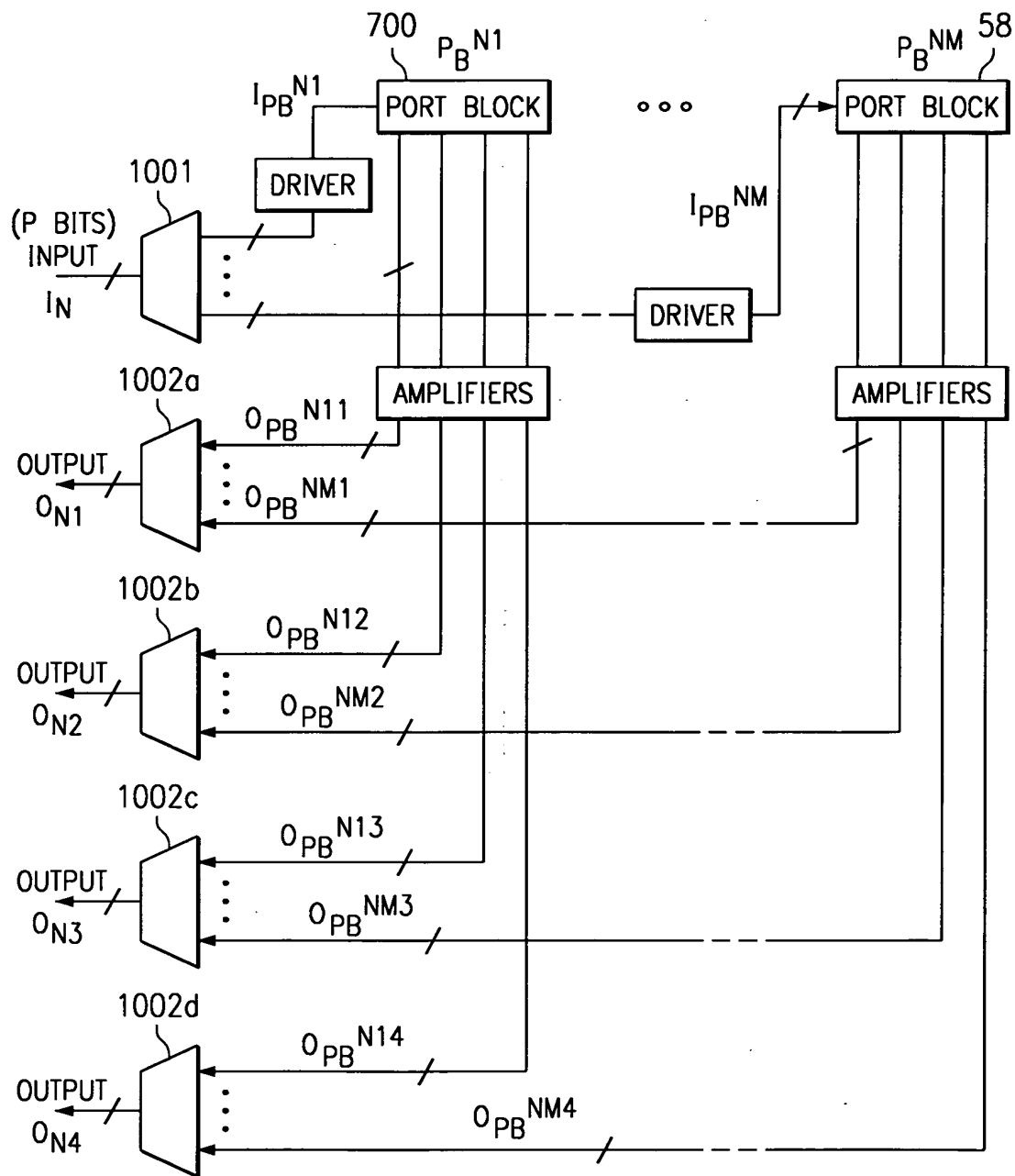


FIG. 11A 7/7

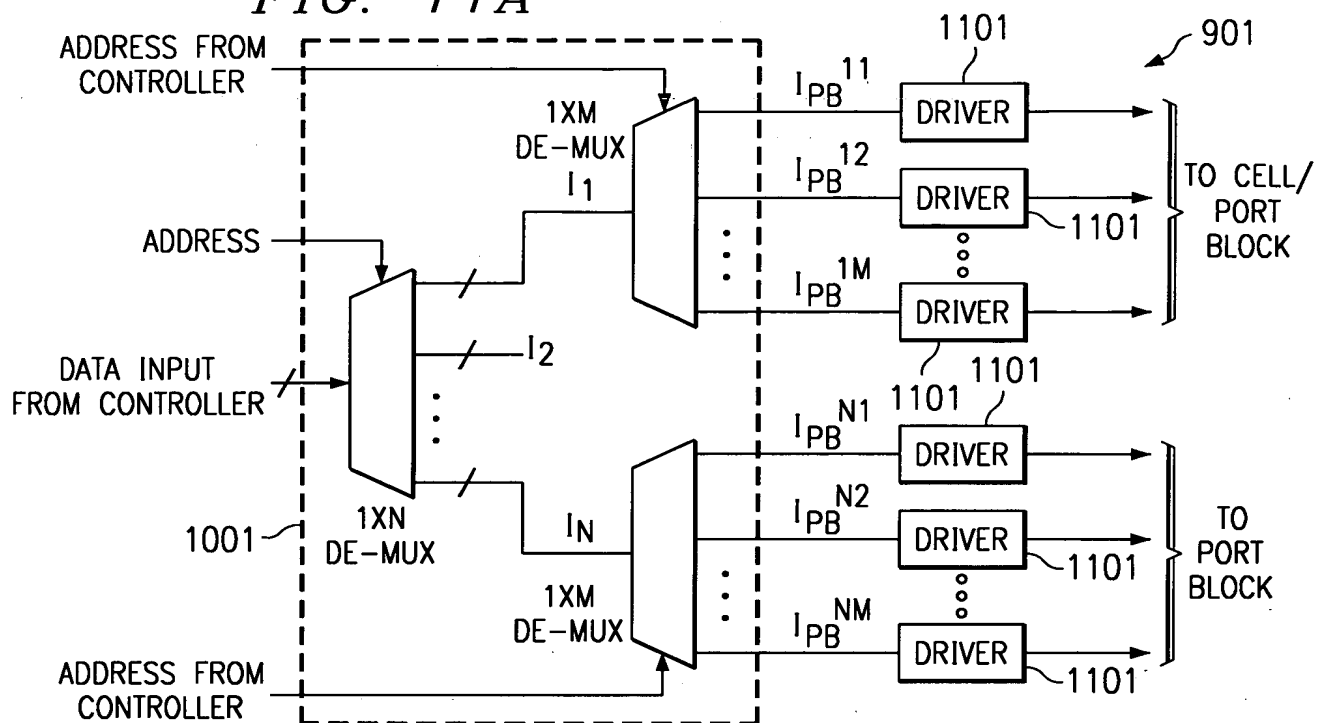


FIG. 11B

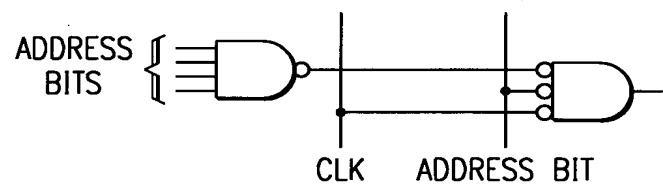


FIG. 12

